

-(Approved by AICTE, Affiliated to MGU / APJ Abdul Kalam Technological University, NAAC Accredited & ISO Certified Institution)-

	CERTIFICATE PROGRAMS- ACADEMIC YEAR 2017-2018					
SIN	o Name of Program	Date & Duration	Agency Conducted	No of students enrolled (without repeating)	No of Times held in an year	
	PC Hardware and					
2.17	1 Networking	5/8/2017,30 hrs	MLMCE	14	1	
	2 Certification on Python	7/10/2017,30 hrs	MLMCE	12	1	
		08-01-2018 & 30				
	3 HDL Design	HRS	MLMCE	163	1	
	2D to 3D IC-change in	21.02.2010.0.00			- C-+	
4 64		121-03-2018 & 30				
	+ ic design	HRS	MLMCE	163	1	
ł	5 E-TAP	20/01/2018 & 30 hours	MLMCE	38	1	
e	E-CAD	12/8/2017 & 30 hours	MLMCE	38	1	
7	Proteus PCB Designing	12/8/2017 & 30 hours	MLMCE	18	1	
8	Latex for begineers	12/8/2017 & 30 hours	MLMCE	40	1	
	Foundation In	19/11/2017,30				
9	Mechanical CAD	Hours	MLMCE	106	1	
10	HVAC	14/10/2017,30 Hours	MLMCE	109	1	
11	NPTEL-SWAYAM	1/1/2017'8WEEKS	MLMCE	2	1	

..... Let a William Ettumanoor KNGALAIN

Registered Office: Mangalam Complex, P.B No.3, S.H Mount, Kottayam, Kerala-686006, Ph:+91-481-2563024



CERTIFICATION ON PYTHON

30 Hour Course

- ✓ Trusted by Recruiters of Software Industry
- ✓ Includes detailed Python Programming
- Available with updated Short Study Material
- ✓ Completion Certificate Will be provided
- ✓ Interaction with Domain Expert

Resource Person

Ms. Nimmymol Manuel, AP, CSE DEPT Ms. Sruthy Emmanuel, AP, CSE DEPT Ms. Divya S.B, AP, CSE DEPT

Date: 07-10-17 to 20-10-17



Coordinator: Ms. Nimmymol Manuel, AP, CSE DEPT

30 Hours Course (Certificate)

Needed to become a Verified Python Programmer

Learn the fundamentals of Python

Become 1 among the trained Engineer

MANGALAM COLLEGE OF ENGINEERING

Mangalam Hills, Vettimukal P O Ettumanoor, Kottayam 686631

Time 9: 00 AM to 5 PM (5 Days)

MANGALAM COLLEGE OF ENGINEERING DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Ref: MLM/CSE/CC/2017-2018/ 02

Date: 22/9/2017

Submitted to the Principal approval

We have proposed to conduct the certificate course on "Certification on Python" for our UG students during this semester (2017-18). Tentative timings and course fees details are mentioned below. Hence, we kindly request you to give approval for the same.

Course Explanation:

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The course is designed to provide Basic knowledge of Python. The course focus on improving the basic programming skills of students using python Programming Language

Course Schedule :- 7/10/2017	to	30/10/2017	7
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5.NO.	Name of the course	Course instructors	Course duration	Approx. Course fee	Target students (year/branch)
2	Certification on Python	Ms.Nimmymol Manuel Ms.Sruthy emmanuel	30 hrs	Free	IV

Thanking You,

Course I/C

Copy to:

- 1. CSE Department file
- 2. Department Notice Board
- 3. IQAC



Principal

PRINCIPAL MANGALAM COL

Internal Quality Assurance Cell (IQAC) Mangalam Coloca CL onineering Fichayana, mara - 635 631

CERTIFICATION ON PC HARWARE & NETWROKING

30 Hour Course

- Trusted by Recruiters of software Industry
- Available with Updated Short Study Material
- Completion Certificate Will be provided
- Interaction with Domain Expert

Resource Person

Ms. Neethu Maria John, AP, CSE DEPT Ms. Tinu Thomas, AP, CSE DEPT Ms.Gayathri R Krishna, AP, CSE DEPT

Date 05-08-17 to 28-08-17



Coordinator: Ms. Neethu Maria John, AP, CSE DEPT

30 Hours Course (Certificate)

Gain knowledge on PC Hardware & Networking

Learn the fundamentals of Hardware & Networking

> Become 1 among the trained Engineer

MANGALAM COLLEGE OF ENGINEERING

Mangalam Hills, Vettimukal P O Ettumanoor, Kottayam 686631

Time 9: 00 AM to 5 PM (5 Days)



Ref: MLM/CSE/CC/2017-2018/ 01

Date: 24/07/2017

Submitted to the Principal approval

We have proposed to conduct the certificate course on "PC Hardware and Networking" for our UG CSE students during this semester (2017-18). Tentative timings and course fees details are mentioned below. Hence, we kindly request you to give approval for the same.

Course Explanation:

PC Hardware and Networking focus to train the students to acquire basic knowledge in computer hardware and peripherals for installation, PC assembly, trouble shooting and maintenance including system management and its backup. The course also provides a basic knowledge of TCP/IP networks work group, internet and intranet.

Course Schedule :- 5/08/2017 to 28/8/2017

S.No.	Name of the course	Course instructors	Course duration	Approx. Course fee	Target students (year/branch)
1		Ms.Neethu Maria			
2	Certification on PC Hardware & Networking	Ms.Tinu Thomas	30 hrs	Free	III&IV CSE
3		Ms.Gayathri R Krishna			Dept.

Internal Quality Ass tra

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Kulityani, mula - 100 631

Thanking You,

Course I/C

Copy to:

- 1. CSE Department file
- 2. Department Notice Board
- 3. IQAC

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- 3. Network basic and configuration
 - 5.1 Setting IP addresses,
 - 5.2. Sharing files and folders
 - 5.3. Network troubleshooting
 - 5.4 PINCI test, ipconflg etc

Introduction to servers and network security

6.1 Appearof servers.

0.1.1 Liles servers.

- 6.1.2. Email Servers,
- 613 Proxy servers etc.
- 6.2 Basics of Internet and Intranet:
- 6.3. Types of Internet connections
 - 6.3.1 Dialup, Broadband, Leased Line, Wi-Fi, Wi-Max, 2G, 3G, 4G, WWW, U-mails, Search Ungines, Social Networking.

.

- 0.3.2 Cloud application
- 6.3.3 Audio-video Conferencing
- 6.3.4 Voice over Internet Protocol (VOIP).
- 6.4. Recovery and backup.
- 6.5. Essential security measures

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Prefsared By Ms. Neethu Maria John

Approved by

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CERTIFICATE COURSE IN HVAC

30 Hour Course

- Includes detailed technologies in HVAC
- Available with updated Short Study Material
- Completion Certificate Will be provided
- Interaction with Domain Expert

Resource Person

Mr. Hanprasad K S APME MANGALAM COLLEGE OF ENGINEERING

Date: 14/10/2017 to 16/12/2017



Learn the fundamentals of HVAC

Vettimukal E.O.

MANGA AM CONFERENCE ENGINEERIN



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MANGALAM COLLEGE OF ENGINEERING DEPARTMENT OF MECHANICAL ENGINEERING

Course on Heat Ventilation & Air Conditioning (HVAC) Syllabus

Course Code: MEACI ⁻ HVAC Pre-requisite: NIL		Course on HVAC		т	
				0	
Module	ule Topics			L	
1	Introduction to HVAC Fundamental of Heat Transfer Fundameticals for RAC Refrigerants in HVAC Fundamental of Psychrometric Psychrometric Process			6	
2	Classification of AC, Non-Centralized & Centralized AC, Air Handling Unit, Chiller Water System, Package System, VRV & VRF System				
3	Introduction to Heat Load, Heat Load Calculation for Bestoom. Heat Load Calculation for Hostel, Heat Load Calculation for Dimning Introduction to Hourly Analysis Program, Heat Load Calculation for HAP				
÷.	Introduction about Ducting, Duct Fittings, Duct Annotation. Single Line Diagram, Double Line Diagram. Duct Design				
2	Introduction to Hydronic System, Pipe Design, Fittings, Types of Valves, Pipe Loss Calculation and Pump Selection, Ventilation, Fresh Air Circulation, Clean Room				
	То	tal Hours			
= Mod differ	le: Flipped Class Room [Le ent types of equipment's, Leo	cture to be videotaped]. Industrial visit to see ctures by Experts from Industry (two or more sessions)			

Internal Quality Assurance Cell (IQAC) Mangalom College of Engineering Kottayam, India - 686 631

MANGALAM COLLEGE OF ENGINEERING



MANGALAM COLLEGE OF ENGINEERING DEPARTMENT OF MECHANICAL ENGINEERING

	La Lania Vour	: 2017-18	
1.	Academic y cai	Uset Ventilation & Air Conditioning	
2	Tile of the Course	: Treat ventuation et rus	
3.	Objective of the Course	: To provide an overlook on the value aspects of HVAC	
4.	Prerequisite	: Nil	
5	Requirement	: Seminar Hall	
	D maficiary	: S7 Students	
6.	Beneficially	: 14.10.2017 - 16.12.2017 (30 Hours)	
7.	Date and Duration of the Course		
8.	Proposed Timing	: 9:00 AM – 12:00 PM (3 hours per day)	
0	No of Hours Required	: 30 Hours	
9.		: Mr. Hariprasad K S AP (ME)	
10.	Internal Resources	· Nil	
11.	Course Registration Fees		
12.	Contents of Courses	: Enclosed Separately	
13		: Those who have 80 % of Attendance	
	Credits and Certification	and scored 60 % in internal exam	
	Creans and Certification	evaluation	
		· Mechanical Seminar Hall	
14.	Venue	, monument e	

Department of Mechanical Engineering

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COURSE COORDINATOR



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CERTIFICATE COURSE IN FOUNDATION IN MECHANICAL CAD

30 Hour Course

- ✓ Includes detailed technologies in CAD
- Available with updated Short Study Material
- ✓ Completion Certificate Will be provided
- ✓ Interaction with Domain Expert

Resource Person

Mr. Sammon Korah APME MANGALAM COLLEGE OF ENGINEERING

Date: 18/11/2017 to 28/11/2017



Learn the fundamentals of MECHANICAL CAD

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SYLLABUS (Add-on Course)

Subject Name: Foundation in Mechanical CAD Subject Code: SCADM0075

Total hours: 30

MODULE 1: (8 hours)

Engineering Graphics – Industrial Overview, Codes &Standards, Projection, Industrial projects, AutoCAD Interface Line, Circle, Trim, Inquiry-Distance, Rectangle, Copy, Move, Rotate, Poly line, Mirror, polygon, Stretch, Fillet, Chamfer, Elipse are, Region Array, Splines, Practice sessions

MODULE 2: (12 hours)

Single Line Text, Multiline Text, Explode, Hatch & Gradient, Drafting settings, Properties Tool Bar, Block (Make Block, Insert Block), Dimension style, Table Style, Isometric Drawings, Company Layout Designing, Drawings in Layer-International projects, Properties, Multi leader style

MODULE 3: (10 hours)

Design centre, Attribute, Inquiry – Area, Distance etc, Quick select, Filter, Draw order, Multi line style, Multi Line, Drawing Limits, Raster Image Reference, Hyper link, External References, Model-Title Block, Lay out ,Scaling, Scale List, View port, Plot Style manager

IQAC

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MANGALAM COLLEGE OF ENGINEERING DEPARTMENT OF MECHANICAL ENGINEERING

1.	Academic Year	: 2017-18
2.	Tile of the Course	: Foundation in Mechanical CAD
3	Objective of the Course	
4.	Prerequisite	: Nil
5.	Requirement	: Seminar Hall, CAD LAB
0,	Beneficiary	: S1 & S2 Students
7.	Date and Duration of the Course	:19.11.2017 to 28.11.2018(30 Hours)
8.	Proposed Timing	: 2:00 PM to 05:00 PM(3 hours per day)
9.	No of Hours Required	: 30 Hours
10.	Internal Resources	: Mr. Sammon Korah AP (ME)
11.	Course Registration Fees	:Nil
12	Contents of Courses	:Enclosed Separately
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation
14.	Venue	:Mechanical Seminar Hall, CAD Lab

Department of Mechanical Engineering

COURSE COORDINATOR

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Minutes of Meeting

All the faculty members of ECE, IQAC coordinator and Placement officer were present in the meeting conducted on 04-01-2018 at Department Library to finalize the Certificate course to be offered for the current academic year to bridge the gap identified in the Curriculum.

After analyzing the feedback received from various stakeholders like students, faculty. Alumni and Employers, the gap in the Curriculum prescribed by KTU is identified. Further the discussion is held on selection of Certificate course on par with the Standards of premier institutes and Industry expectations. Finalized the following Certificate course and guest lectures for the Academic Year 2017-18.

Details of Certificate course:

S. N o	Name of Certificate Course	Year/ Semester	Total No of Hours	Date of Commencement	Faculty Coordinator
1	HDL Design	S4, S6 & S8	30	08-01-2018	Prof Jyothisree K RAssoc.Prof/EC E
2	2D to 3D IC-change in trends in semiconductor IC design	S4, S6 & S8	30	21-03-2018	Prof Simi P Thomas, Ap/ECE

The faculty members will be initiated at the earliest to design the curriculum for the specified Certificate course after getting approval from the Principal.

HoD - ECE

PRINCIPAL MANGALAM COLLEGE OF ENGINEERING Ettumanoor



HDL DESIGN

30 Hour Course

- ✓ Trusted by Recruiters of VLSI Industry
- ✓ Includes detailed VHDL and Verilog Programme
- ✓ Available with Updated Short Study Material
- ✓ Completion Certificate Will be provided
- ✓ Interaction with Domain Expert

Resource Person

Mr Abraham C G Researcher AnnaUniversity, Chennai

Date: 08-01-2018 to 12-01-2018



Coordinator: Prof JyothiSree K R, AP-ECE

30 Hours Course (Certificate)

Needed to become a Verification Engineer

Learn the fundamentals of VHDL and Verilog

Become 1 among the trained Engineer

MANGALAM COLLEGE OF ENGINEERING

Mangalam Hills Vettimukal P O Ettumanoor, Kottayam 686631

Time 9: 00 AM to 5 PM

MANGALAM COLLEGE OF ENGINEERING



Department of Electronics and Communication Engineering

1.	Academic Year	: 2017-18
2.	Tile of the Course	: HDL Design
3.	Objective of the Course	:To develop the skill for HDL Design
4.	Prerequisite	: Basic Programming
5.	Requirement	: Online Google meet for theory class : Project Lab for practical class
6.	Beneficiary	: S8, S6, S4 Students
7.	Date and Duration of the Course	: 08-01-2018 to 12-01-2018
8.	Proposed Timing	: 09.00 AM to 04.00 PM
9.	No of Hours Required	: 30 Hours
10.	Resource Person	: Mr Abraham C G
11.	Course Registration Fees	: Nil
12.	Contents of Courses	: Enclosed Separately
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation
14.	Venue	: Online / afflime V

COURSE COORDINATOR

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LEGE OF ENGINEERING MANGALAM COL Ettumanoor

CERTIFICATE COURSE

HDL Design

Course Objective:

- > To expose the students to the basic fundamentals of HDL
- > To discuss VHDL in detail
- > To discuss Verilog in detail

Course Outcomes:

- > Student knows and how to install and write coding for HDL
- > Student learns to write programmes for various Digital logic circuits

S.NO.	CONTENT	INSTRUCTIONAL
		HOURS
	Section 1	
1	Module:-1 - VHDL(4 HRS)	4
	VHDL's history. Benefits of VHDL. Concepts of VHDL.	
	Entity and Architecture. Behavioral,	
	data flow and structural specifications. Mixed structural-	
	behavioral models. Syntax of VHDL.	
2	Module:-2 - VHDL(6 HRS)	6
	Basic elements of VHDL. Timing model. Delay types: delta,	
	transport and inertial delays. Data	
	types. Scalar data types: numerical, physical (operations with	
	physical types, time description),	
	enumerated. Attributes of scalar data types. Conversion of	
2	Scalar data types. Subtypes.	6
3	Module:- 3- Verilog (0 HKS)	0
	methodologies Basic concepts - Module Data	
	times: nets registers vectors arrays Parameter times	
	Operators Operator types precedence	
	Sequential and parallel blocks. Comparison of sequential and	
	parallel blocks. Basic compiler	
	directives Behavioral modeling	
	Section 2	
10	Madula: 4 Mariles (6 HDS)	
10	Repaired modeling blocks shows block event has t	0
	timing control branch statements acco	
	Broadural assignments, blacking and non-blacking. Det	
	flow modeling. Assign statements	
	Delays Implicit not declaration. Cate level we delive C.	
	types: and/or buf/not gates	
	hypes, and/or, our/not gates,	
	block LIDP, Ports, Port connection	
	rules: by order and normal	
11	Module: 5 Veriles (9 JD G)	
11	iviouule:-5 verilog (8 HKS)	8

Switch level modeling. Primitives. Use of tri-reg. Test-bench	
creation. Initial block. Delay-based	
timing control. System tasks. Monitoring a simulation.	
Looping constructs: while loop, for loop,	
repeat, forever loop. VCS simulation examples. VCD file	
fragment. Tasks and functions.	
Differences between tasks and functions.	
Total Hours	=30

Recommended Text Books:

- 1. Palnitkar, Samir. Verilog HDL: a guide to digital design and synthesis. Vol. 1. Prentice Hall Professional, 2003.
- 2. Minns, P. D., & Elliott, I. (2008). FSM-based digital design using Verilog IIDL. John Wiley & Sons.

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2D TO 3D IC CHANGE IN TRENDS IN SEMICONDUCTOR IC DESIGN

30 Hour Course

- Trusted by Recruiters of Semiconductor Industry
- ✓ Includes detailed IC Design Flow
- Available with updated Short Study Material
- ✓ Completion Certificate Will be provided
- ✓ Interaction with Domain Expert

Resource Person

Prof Radeep Krishna R Centre for VLSI Design KARE , Madurai Date: 21-03-2018 to 25-03-2018



Coordinator: Prof Simi P Thomas, AP/ECE

30 Hours Course (Certificate)

Needed to become an Physical Design Engineer

Learn the fundamentals of VLSI Physical Design

Become 1 among the trained Engineer

MANGALAM COLLEGE OF ENGINEERING

Mangalam Hills, Vettimukal P O Ettumanoor, Kottayam 686631

Time 9: 00 AM to 5 PM (5 Days)

MANCALAN COLLEGE OF ENGINEERING



Department of Electronics and Communication Engineering

1.	Academic Year	: 2017-18
2.	Tile of the Course	: 2D to 3D IC-change in trends in semiconductor IC design
3.	Objective of the Course	:To develop the skill for VLSI Physical Design
4.	Prerequisite	: Basic VLSI
5.	Requirement	: Online Google meet for theory class : Project Lab for practical class
6.	Beneficiary	: S8,S6,S4 Students
7.	Date and Duration of the Course	: 21-03-2018 to 25-03-2018
8.	Proposed Timing	: 09.00 AM to 04.00 PM
9.	No of Hours Required	: 30 Hours
10.	Resource Person	: Prof Radeep Krishna R
11.	Course Registration Fees	: Nil
12.	Contents of Courses	: Enclosed Separately
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation
14.	Venue	: At ECE Department

COURSE COORDINATOR

HOD/ECE

PRINCIPAL

PRINCIPAL MANGALAM COLLEGE OF ENGINEERING

17

CERTIFICATE COURSE

2D to 3D 1C-change in trends in semiconductor IC design

Course Objective:

- > To expose the students to the basic fundamentals of VLSI Physical Design
- > To understand the 2D IC Design
- > To understand the 3D IC Design

Course Outcomes:

- Student knows and how to develop optimisation algorithms for different VLSI Physical Design Flows
- Student learns to do optimisation in 2D IC and 3D IC Designs

S.NO.	CONTENT	INSTRUCTIONAL HOURS	
	Section 1		
Section		2	
1	Introduction to VLSI Physical Design	2	
2	2D IC Design	2	
3	3D IC Design	2	
4	VLSI Multi-Partitioning	4	
5	VLSI Non Slicing Floor planning	4	
6	VLSI Placement	4	
7	VLSI Routing	4	
8	Timing Analysis	4	
Section 2			
10	Example's	4	
10	Total Hours	=30	

Recommended Text Books:

- Todri-Sanial, Aida, and Chuan Seng Tan, eds. *Physical Design for 3D Integrated Circuits*. CRC Press, 2017.
- 2. Kahng, A. B., Lienig, J., Markov, I. L., & Hu, J. (2011). VLSI physical design: from graph partitioning to timing closure. Springer Science & Business Media.

PRINCIPAL MANGALAM COLLEGE OF ENGINEERING



MANGALAM COLLEGE OF ENGINEERING Mangalam Hills, Vettimukal p.o Ettumanoor, Kottayam 686631



Date: 12/08/2017

(30 hour course)

(certificate)

Organized by : Department Of Electrical And Electronics Engineering

Speaker: Liya Aliyas

(Assistant Proffesor Department of Electrical And Electronics Engineering)





Department of Electrical and Electronics Engineering

۱.	Academic Year	: 2017-2018
2.	Tile of the Course	: Proteus PCB Designing
3.	Objective of the Course	To understand designing of PCB using software, To designing an integral part of each electronics products
-1.	Prerequisite	: NIL
5.	Beneficiary	: S5Students
	Date and Duration of the	: 12.08.2017 to 14.10.2017 (30
б.	Course	Hours)
7.	Proposed Timing	: 09.00 AM to 04.00 PM(6 hours per every Saturday)
8	No of Hours Required	: 30 Hours
g -	Internal Resources	: Ms. Liya Aliyas, AP/EEE
10	Course Registration Fees	:Nil
10.	Contents of Courses	Enclosed Separately
11.	Content	: Those who have 80 % of
	Credits and Certification	Attendance and scored 60 % in
12.	Credits and Contineation	practical evaluation
		: CAD Lab for practical class and S
13.	Venue	Class room

COURSE COORDINATOR

HOD/EEE

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CERTIFICATE COURSE

Proteus PCB Designing

Course Objective:

This is a basic course for designing of PCB using software. PCB (Printed Circuit Board) designing is an integral part of each electronics products and this program is designed to make students capable to design their own projects PCB up to industrial grade.

Course Outcomes:

- Student knows PCB designing and how to install Proteus software.
- Student learn to design electronic circuits.
- Student learn to simulate the circuit with Proteus softwaare.

S.NO.	CONTENT	INSTRUCTIONAL HOURS
	Section 1	
1	Introduction and Application	1
2	What is PCB, Types of PCBs: Single Sided (Single	2
	Layer), Multi-Layer (Double Layer), PCB Materials	
3	Creating a Project	2
4	Planning, Keyboard shortcuts, display options etc	2
5	Components selection and connection in Proteus	3
6	Selection of Controllers	2
7	Scopes, meters and measurement equipment	1
8	Design verification and connections	3
	Section 2	
9	Practical section for basic simpler circuits	2
10	Single Phase Half controlled converter with R load	2 .
11	Single Phase fully controlled bridge converter with R	2
	and	
	RL loads	
12	Three Phase half controlled bridge converter with	2
	Rilload	
13	Single Phase AC Voltage Controller with R and RL	2
	Loads	
14	Single phase Inverter with PWM control	1
15	Single Phase series inverter with R and RL loads	1
16	Buck and Boost converter	2 .
	Total Hours	=30

Recommended Books:

1. Proteus (Design Software) by Lambert M. Surhone, Miriam T. Timpledon, Susan F. Marseken, VDM Publishing, 2010.

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MANGALAM COLLEGE OF ENGINEERING Mangalam Hills, Vettimukal p.o Ettumanoor, Kottayam 686631



Drgunized by : Department Of Electrical And Electronics Engineering

Department of Electrical And Electronics Engineering) (Assistant Proffesor Speaker: AJU THOMAS



CERTIFICATE COURSE

Electrical CAD for Beginners

Course Objective:

> To expose the students to the basic concepts of using the powerful electrical drawing

creation tools in the AutoCAD Electrical software.

> To give Working knowledge of Electrical cad. Student should use Electrical CAD software for Electrical project.

Course Outcomes:

- Student should Know methods of customizing AutoCAD Electrical symbols, circuits, and databases. Other topics covered include title block linking, reporting tools, templates, and Student should Know methods of customizing AutoCAD Electrical symbols, circuits, and

databases

		INSTRUCTIONAL HOURS
	CONTENT	3
S.NO.	Introduction • Autocad Electrical	
	• GUI	3
2	Projects • Introduction to Project Manager	
	 Working with projects 	3
3	 Drawing Create a new drawing Adding a drawing 	3
4	Wires Wire Layers	
	 Wire Types Insert Wire Modify wire 	3
5	Wire Numbers Automatic wire numbers Wire tagging 	
	 PLC I/O wire numbers 	3
6	Grouping Wires	3
7	Point To Point Wiring tools	3
0	DC Lap Winding	3
0	AC Winding	3
10	PROJECT	tal Hours = 30
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Department of Electrical and Electronics Engineering

	Academic Year	2017-2018
	Tile of the Course	Electrical CAD for Beginners
3	Objective of the Course	 To create understanding of the Electrical Cadd.
		: Computer lab for practical class
4	Prerequisite	S7 Students
5.	Beneficiary	12.08.2017 to 14.10.2017 (30 Hours)
6.	Date and Duration of the Course	09.00 AM to 04.00 PM (6 hours per
7	Proposed Timing	every Saturday)
		30 Hours
8	No of Hours Required	Mr. Aju Thomas AP/EEE
9	Internal Resources	Nil
10	Course Registration Fees	NII
10.	Contents of Courses	Enclosed Separately
11	Contest	Those who have 80 % of Attendance
1.7	Credits and Certification	scored 60 % in practical evaluation
12		MANGALAM COLLEGE OF
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Head of Department Planting and Decisioning

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Department of Electrical and Electronics Engineering

1.	Academic Year	: 2017-18
2.	Tile of the Course	: LaTex for Beginners
3.	Objective of the Course	:To create understanding of the LaTeX, To understand the fundamentals of Scilab and Utilization, Development of Proficiency in English and Communication skills
4.	Prerequisite	: Nil
5.	Requirement	: S3 classroom of Dept. of EEE
6.	Beneficiary	: S3 Students
7.	Date and Duration of the Course	: 12.08.2017 to 14.10.2017 (30 Hours)
8.	Proposed Timing	: 09.00 AM to 04.00 PM (6 hours per every Saturday)
9.	No of Hours Required	: 30 Hours
10.	Internal Resources	: Ms. Dhanya S / AP (EEE)
11.	Course Registration Fees	: Níl
12.	Contents of Courses	: Enclosed Separately
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation
14.	Venue	: Dept. of EEE S3 class room

COURSE COORDINATOR

HOD/EEE

Head of D-partment Electric L od Electronics

PRINCIPAL

PRINCIPAL MANGALAM COLLEGE OF ENGINEERIN MANGALAM COLLEGE OF ENGINEERING

CERTIFICATE COURSE

LaTex for Beginners

Course Objective:

- > To expose the students to the basic concepts documentation and its importance.
- > To give Working knowledge of Latex typesetting language.
- Student should use Latex file to define document class and layout options.
- Student should use various methods to either create or import graphics into Lack document.
- Student should use tabular and array environments within Latex documents.

Course Outcomes:

- > Student knows and how to install Latex software.
- > Student learns to write equations, matrix and tables.
- > Student learn to quote the references, equation references, citations.
- > Student lists the figures, tables and generating index.

SNO	CONITENT	INSTRUC	CTIONAL
5.100.	CONTENT	HOURS	<u> </u>
	Section 1		5-515
1	Introduction and Application	1	
2.	Installation of the software LaTeX and basic MITEX	2	
- <u></u> 2. 3	Understanding Latex compilation Basic Syntax	2	े <u>ग</u>
	Page Lavout – Titles, Abstract Chapters, Sections,	3	
ч.	References, Equation references, citation.		
5	Classes: article, book, report, beamer, slides, IEEE tran.	2	
<u>5</u>	List making environments	2	
0.	Table of contents, Generating new commands		
7	Errors and Error handling	1	
8	Font Effects: Coloured Text, Font Sizes, Lists, Comments &	2	
0.	Spacing, Special Characters		
Section 2			
9.	Figure handling numbering and List of figures with Practical	2	
	examples		
10	Tables and Practical examples	3	
11	Writing equations, Matrix, Mathematical Equations and	2	
11	numbering methods		
12	Mathematical Symbols and format	2	
13	Practical on numbering and without numbering format	1	·
14	Inserting References: Introduction, The BibTeX le	1	
15	Inserting the bibliography, Citing, Styles.	2	
15	Practical examples	2	
10	Total Hours	=30	

Recommended Books:

1. Diller, Latex Line by Line, published by Wiley.

2. Introduction to Latex by Tobias Oetiker.

3. Patrick Daly. Natural Sciences Citations and References, 2006





Department of Electrical and Electronics Engineering

1.	Academic Year	: 2017-18
2.	Tile of the Course	: ETAP Familiarisation
3.	Objective of the Course	:To create understanding of the power system components.To perform modelling and Load flow analysis of a power system using ETAP.To perform short circuit analysis on a small power system.
4.	Prerequisite	: Knowledge of Power System Analysis : Tutorial Sessions in ETAP
5.	Venue	:PG simulation Lab
6.	Beneficiary	: S8 Students
7.	Date and Duration of the Course	: 20/01/2018 to 3/03/2018 (30 Hours)
8.	Proposed Timing	: 09.00 AM to 04.00 PM (6 hours per every saturday)
9.	No of Hours Required	: 30 Hours
10.	Internal Resources	: Mrs.Shoma Mani / AP (EEE)
11.	Course Registration Fees	: Nil
12.	Contents of Courses	: Enclosed Separately
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in practical evaluation



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CERTIFICATE COURSE

ETAP FAMILIARISATION

Course Objective:

- > To introduce the basic concepts in power system modelling.
- To give familiarization of ETAP software for power system analysis and design
- To perform load flow analysis of a small power system using ETAP
- To perform short circuit analysis of a small power system using ETAP

Course Outcomes:

- > Student will be able to model a power system network
- Student acquire skill to perform load flow analysis of a power system network
- Student acquire skill to perform short circuit analysis of a power system network

S.NO.	CONTENT	INSTRUCTIONAL HOURS
	Section 1	
1	Introduction to ETAP	1
2	Why to use a Power System Analysis Software	1
3	Creating New Project in ETAP	1
4	Draw Single Line Diagram	2
5	Load Flow Analysis	2
51	Fundamentals of Load Flow	2
5.2	Load flow calculations	2
5.2.1	Load flow examples using Gauss Seidal Method in ETAP	1
5.2.2	Load flow examples using Newton Raphson Method in	1
5.2.3	ETAP Load flow examples using Fast Decoupled Method in	1
	ETAP Section 2	
	Ly in the Short Circuit Analysis	2
6	Introduction to Short Circuit Analysis	3
6.1	Fundamentals of Short Criterions	3
6.2	Short Circuit Hand Calculations	4
6.2.1 6.2.2	Symmetrical and Unsymmetrical Faults Short Circuit Analysis examples demonstration using	4
	ETAP Total Hours	=30

Recommended Books:

1. ETAP for Electrical Engineers



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Consolidated Report for the Certificate Course –Electrical CAD for Beginners

Academic Year: 2017-2018

Batch 2014-2018

Year: IV/VII Semester

	· Cortificate Course
Name of the Activity	: Certificate Course
	El cial CAD for Deginners
Title of the Activity	. : Electrical CAD for Beginners
Staff In charges	:Mr. Aju Thomas AP/EEE
5 mil 2 mil 2 mil 6	
Place of the Activity	:Mangalam College of
	Engineering Ettumanoor
	Engineering.Ettumanoor.
	: 38
No. of Participants	
	.38
No. of Qualified	
	. Mr. Aiu Thomas A P/EEE
Name & Designation of Expert	. MI. Aju momas Al'ILLL,
	The survey the students to the basis
Objective of the Activity	To expose the students to the basic
	concepts of Electrical drawing using
	Electrical CAD
Outcome of the Activity	: Students were able to understand and
	draw electrical diagrams using Electrical
	CAD

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Consolidated Report for the Certificate Course - LaTex for Beginners

Academic Year: 2017-18

Batch 2016-2020

Year: II/III Semester

Name of the Activity	: Certificate Course
Title of the Activity	: LaTex for Beginners
Staff Incharges	:Ms. Dhanya S , AP/EEE
Place of the Activity	: Dept. of EEE
	Mangalam College of Engineering.
	Ettumanoor.
No. of Participants	: 40
No. of Qualified	: 40
Name & Designation of Expert	: Ms. Dhanya S , AP/EEE
Objective of the Activity	To expose the students to the basic concepts documentation and its importance and to give Working knowledge of Latex typesetting language.
Outcome of the Activity	: Students understood how to frame report and thesis, Improved their proficiency in English and communication skill

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Consolidated Report for the Certificate Course - Proteus PCB Designing

Academic Year: 2017-2018

Batch 2015-2019

Year: III/V Semester

Name of the Activity	: Certificate Course
Title of the Activity	: Proteus PCB Designing
Staff Incharges	: Ms. LiyaAliyas, AP/EEE
Place of the Activity	: Dept. of EEE
n an	S5 classroom and CAD Lab
No. of Participants	: 18
No. of Qualified	: 18
Name & Designation of Expert	: Ms. LiyaAliyas, AP/EEE
Objective of the Activity	:This program is designed to make students capable to design their own projects PCB up to industrial grade
Outcome of the Activity	: Students were able to design basic converters and their controlling
	networks in Floteus

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Consolidated Report for the Certificate Course - ETAP FAMILIARISATION

Academic Year: 2017-2018

Batch 2014-2018

Year: IV/VIII Semester

Name of the Activity	: Certificate Course
Title of the Activity	: ETAP FAMILIARISATION
Staff Incharges	: Mrs.Shoma Mani, AP/EEE
Place of the Activity	: Dept. of EEE
	Mangalam College of
	Engineering.Ettumanoor.
	PG Simulation Lab
No. of Participants	: 38
No. of Qualified	: 38
Name & Designation of Expert	: Mrs.Shoma Mani, AP/EEE
Objective of the Activity	To create understanding of the power system components and do Load flow analysis in G-S,NR and F-D methods using ETAP.To perform short circuit analysis on a small power system
Outcome of the Activity	: Students understood how to do load flow analysis, short circuit analysis and cable sizing of conductors.



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Consolidated Report for the Certificate Course - HDL Design

Academic Year: 2017-18 ECE Year: IV , III , II

Batch S8, S6, S4-

Name of the Activity	: Certificate Course
Title of the Activity	: HDL Design
Staff In charges	: Prof Jyothisree K R. Assoc Prof ECE
Place of the Activity	: Dept. of ECE
	Mangalam College of Engineering Ettymonoor
a fi	Mungalam Conege of Englicering. Ettumatioor.
No. of Participants	$\cdot 45 + 45 + 73 = 163$
r (or of i articipants	. +3 + 43 + 75 - 105
No of Qualified	· 15+15+72-162
rio. of Quanned	. +5++5+75-105
Name & Designation of Export	Mr Abrohom C.C. Dessention A. II : : : CI
Name & Designation of Expert	. Wir Abraham C. G, Researcher, AnnaUniversity, Chennai
Objective of the Activity	Learn the fundamentals of HDL Designs
*	
Outcome of the Activity	Participants learned theory and practiced the development
•	of VHDL and Verilog
	of villed and velling

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Consolidated Report for the Certificate Course – 2D to 3D IC-change in trends in semiconductor IC design

Academic Year: 2017-18 Year: IV,III,II

Batch S8,S6,S4-ECE

Name of the Activity	: Certificate Course
Title of the Activity	: 2D to 3D IC-change in trends in semiconductor IC
	design
Staff In charges	: Prof Simi P Thomas
Place of the Activity	: Dept. of ECE
	Mangalam College of Engineering. Ettumanoor.
No. of Participants	: 45+45+73=163
No. of Qualified	: 45+45+73=163
Name & Designation of Expert	: Prof Radeep Krishna R, Centre for VLSI Design, KARE
Objective of the Activity	Learn the fundamentals of VLSI Physical Design
Outcome of the Activity	Participants learned theory and practiced the development
	of VLSI Physical Design

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Department of Computer Science And Engineering

1.	Academic Year	: 2017-18	
2.	Tile of the Course	PC Hardware and Networking	
3.	Objective of the Course	The main objective of the course is to provide the students with a concrete foundation in PC Hardware and Networking.	
4.	Prerequisite	: Nil	
5.	Requirement	: Seminar Hall	
6.	Beneficiary	: S3/S5 Students	
7.	Date and Duration of the Course	: 05-08-2017 to 28-8-19	
8.	Proposed Timing	: 9.00 AM to 05.00 PM (6 hours per day)	
9.	No of Hours Required	: 30 Hours	
10.	Internal Resources	: Ms Neethu Maria John	
11.	Course Registration Fees	: Nil	
12.	Contents of Courses	: Enclosed Separately	
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation	
14.	Venue	: Seminar Hall	

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Department of Computer Science And Engineering

1.	Academic Year	: 2017-18	
2.	Tile of the Course	Certification on Python	
3.	Objective of the Course	The main objective of the course is to provide the students with a concrete foundation in Certification on Python.	
4.	Prerequisite	: Nil	
5.	Requirement	: Seminar Hall	
6.	Beneficiary	: S7	
7.	Date and Duration of the Course	: 07-10-2017 to 30-10-2017	
8.	Proposed Timing	: 9.00 AM to 05.00 PM (6 hours per day)	
9.	No of Hours Required	: 30 Hours	
10.	Internal Resources	: Ms Nimmymol Manuel	
11.	Course Registration Fees	Nil	
12.	Contents of Courses	: Enclosed Separately	
13.	Credits and Certification	: Those who have 80 % of Attendance and scored 60 % in internal exam evaluation	
14.	Venue	: CSE Seminar Hall	



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Certificate Course - Foundation in Mechanical CAD

Detailed explanation about the course

Academic Year: 2017-2018

Batch 2017-2021

Year: I & II Year

Department of Mechanical Engineering organized a certificate course on Foundation in Mechanical CAD for the First & Second Year B. Tech students (2017-2021 Batch) from 19th November 2017 to 28th November 2017. A total of 106 students were enrolled for the course. 22 lecture hours and 8 practical hours were provided during the course as per curriculum. A multiple choice based examination was conducted for the enrolled students at the end of the course. Certificates were provided for the participants after successful completion of the course.

No of students Registered	No of Students participated	No of Students Passed	No of students Eligible for Certificate
106	106	106	106
يصيمها			

Course Coordinator

HOD / ME



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Certificate course: Heating, ventilation and Air-conditioning

Detailed Explanation of Course

Academic Year: 2017-2018

Batch: 2014-2018

Year: IV/VII Semester

The main objective of the course is to provide the students with a solid foundation in heat ventilation and air condition and make them understand the calculation for heat loss and also about duct design.

The course was offered for finalyear mechanical students of Mangalam college of engineering. The course was designed with five modules and course was conducted from 14.10.2017 to 16.12. 2017.Certificate was provided with students those who have 80 % of Attendance and scored 60 % in internal exam evaluation

No of students Registered	No of Students participated	No of Students Passed	No of students Eligible for Certificate
109	109	109	109

Course Coordinator

HOD/ME

